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**REPLY UNDER 37 CFR 1.116
EXPEDITED PROCEDURE
TECHNOLOGY CENTER 2800**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.:	10/711,959	Conf. No.:	5958
Filing Date:	10/15/2004	Art Unit:	2825
Applicant:	Heng et al.	Examiner:	Lam, Nelson C.
Title:	INTEGRATED CIRCUIT SELECTIVE SCALING	Docket No.:	BUR920040201US1 (TBM-0059)

COMMISSIONER FOR PATENTS

DESTINATION FACSIMILE NUMBER: 571-273-8300

Transmitted herewith is: **After-Final Amendment in 14 pages**
in the above identified application.

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Signature

August 21, 2006
Date

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AFTER-FINAL AMENDMENT

Sir:

I. INTRODUCTORY COMMENTS

In response to the Final Office Action of June 20, 2006, please amend the above-referenced patent application as follows: